United States Patent [19]

Sherrod

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[54] **METHOD AND APPARATUS FOR SCHEDULING THE EXECUTION OF MULTIPLE PROCESSING TASKS IN A COMPUTER SYSTEM**

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[58] Field of Search ... 364/200 MS File, 900 MS File, 364/300

[56] **References Cited**

U.S. PATENT DOCUMENTS

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[57] **ABSTRACT**

A task scheduler for scheduling the execution of a plurality of tasks within a computer system. The task scheduler utilizes a combination of externally assigned priorities and internally calculated priorities to optimize the responsiveness of the computer to external interactions.

4 Claims, 4 Drawing Figures

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